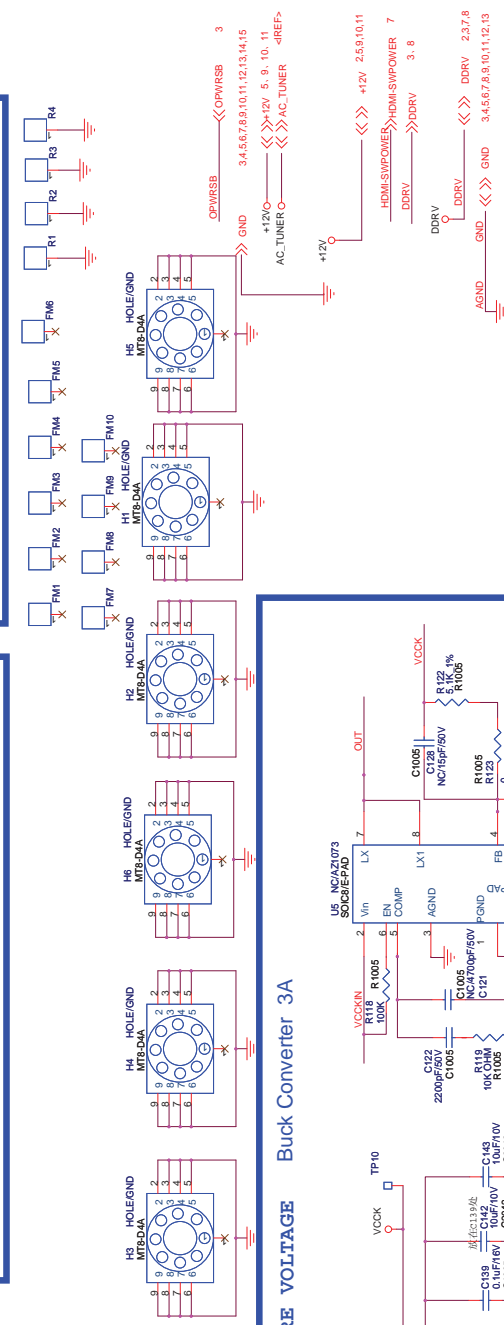
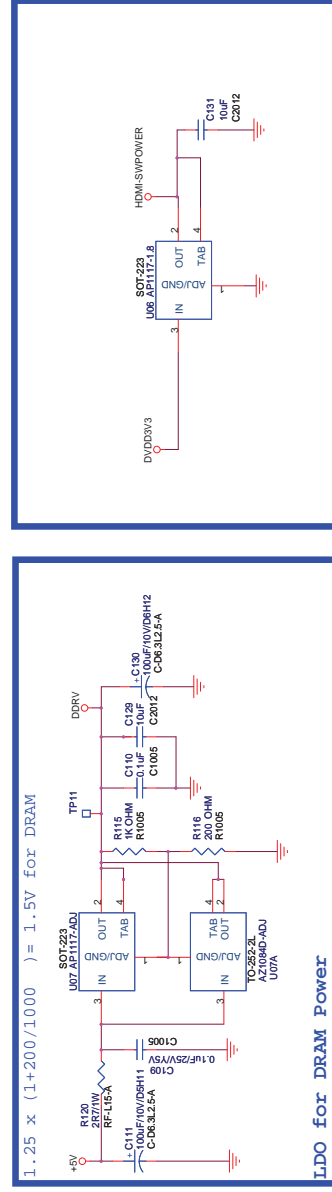
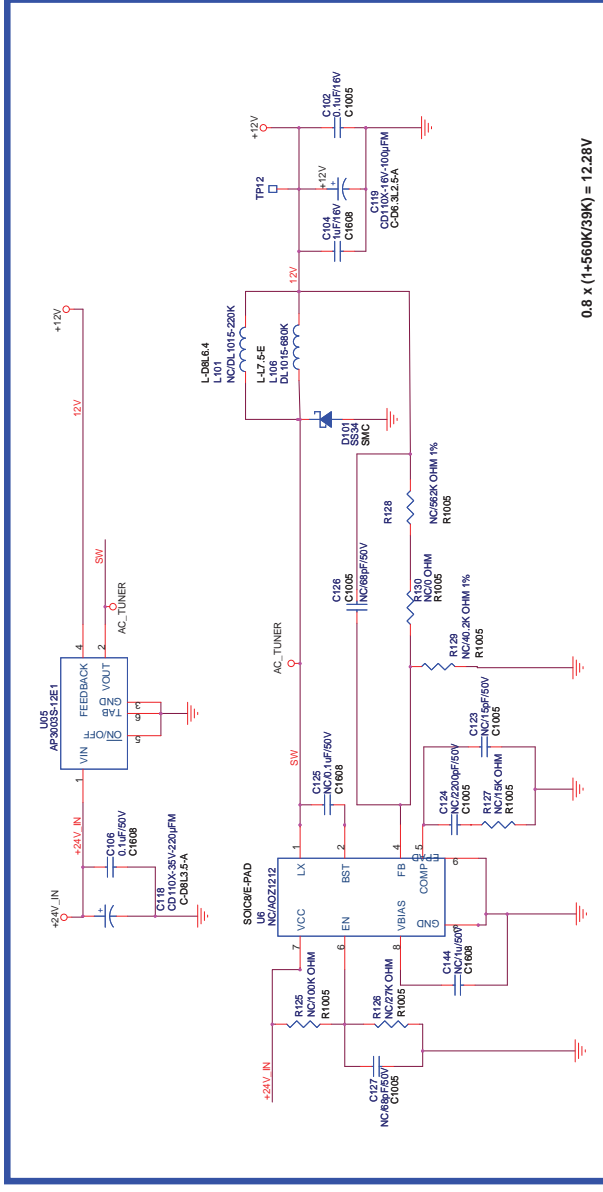
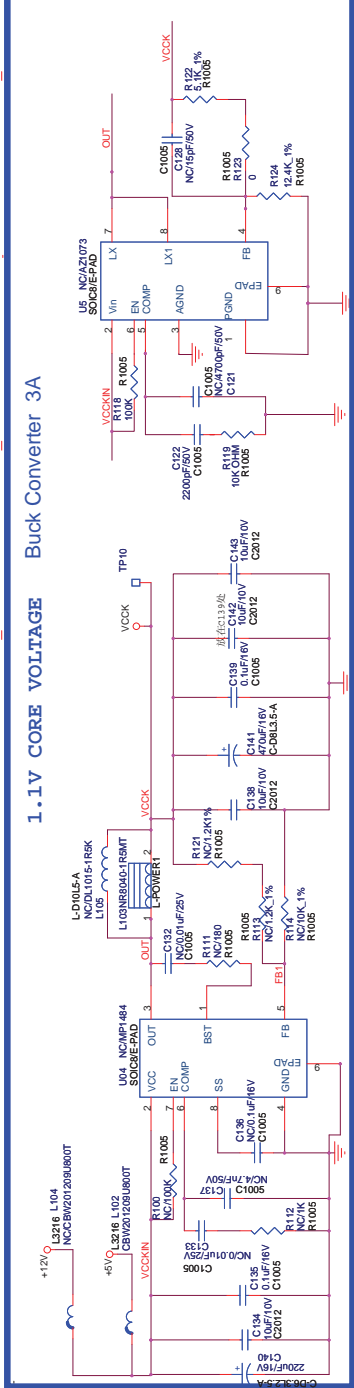
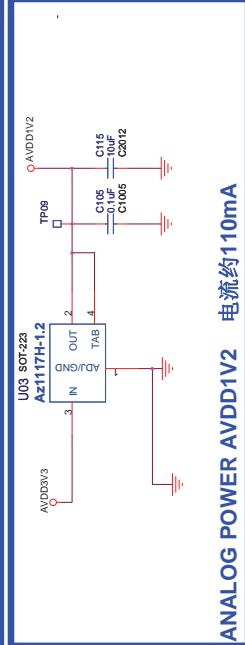
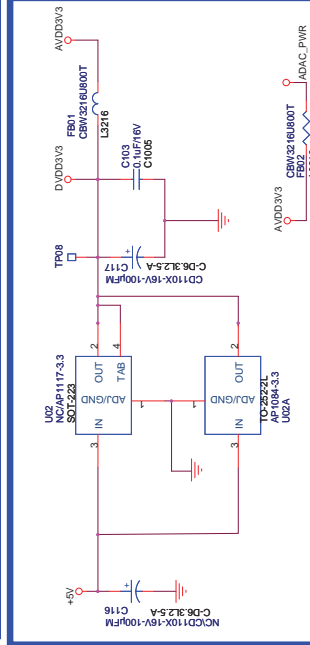
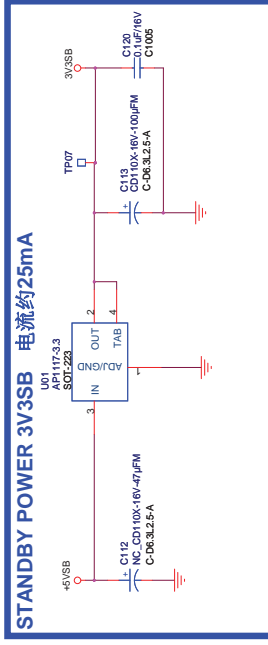
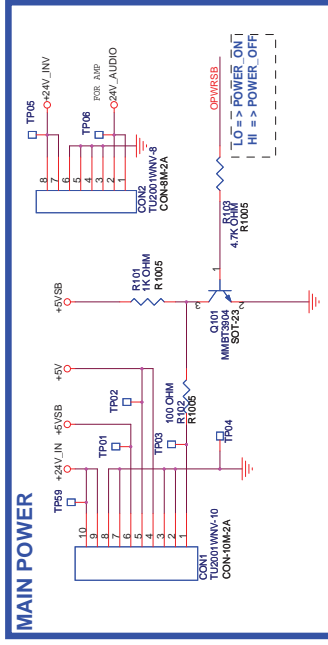


MT5301C (DDR3) VERSION V1.0

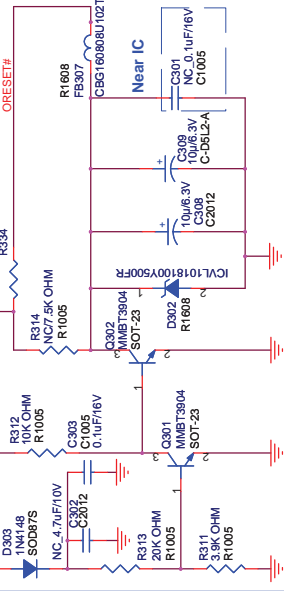
PIN	GPIO	
2	GPIO14	SYS_EEPROM_WP, EEPROM写保护, 高电平写保护
100	AIN_CTL1	4052切换开关
101	AIN_CTL0	4052切换开关
124	HDMI2-SDA	HDMI2-SDA
125	HDMI2-SCL	HDMI2-SCL
126	HDMI_HPD	HDMI 热插拔检测
225	STBL	功放待机控制, 高电平待机
226	MUTE_CTL	功放静音控制, 高电平静音
137	LEDR	指示灯控制, 高电平灯亮
138	LEDG	指示灯控制, 高电平灯亮
139	OPWRSB	电源待机控制, 高电平待机
143	HDMI1-DET	HDMI检测脚
144	HDMI2-DET	HDMI检测脚
145	ADC0_Key	按键2,VOL+, CH+, CH-
146	ADCL_Key	按键1,TV\AV、MENU、VOL-、POWER
207	BL_ON/OFF	背光控制脚, 高电平背光关闭
214	LVDS_PWR_EN	T-CON电压开关, 高电平有电压
130	HDMI1-SCL	HDMI1-SCL
132	HDMI1-SDA	HDMI1-SDA
217	INVERTER_STATUS1	屏状态指示, 低电平正常
223	OPC_ON/OFF	屏OPC功能开关
219	HDMI-RESET	HDMI切换开关复位,低电平复位

DRAM	
DRAM Type	
Single DRAM Chip Size	
DDR3	
2G bit(128M*8*2)	
I2S Out	
I2S MCLK(O)	AOMCLK
I2S BCLK(O)	AOBCK
I2S LRCK(O)	AOLRCK
I2S DATA(O)	AODATA0 (Speaker Out)
Audio CTRL	
All_MUTE_CTRL (O)	ADIN14 (H = UN-MUTE)
Head Phone DET (I)	SPL_DATA(L = Plug-In)
SPDIF IN (I)	NA
Speaker Amp CTRL	
Speaker AMP Type	
AD82581B (Digital SP AMP)	
Speaker Amp Mute (O)	
JTRST (L =MUTE)	
AV Input Source	
CVBS0	CVBS0P
CVBS1	SC0
CVBS2	SY1
SY0	CVBS1P
SC0	SY0
SY1	NA
SC1	NA
YPbPr Input Source	
SOY0	SOY0
Y0P	Y0P
PB0P	PB0P
PR0P	PR0P
SOY1	SOY1
Y1P	Y1P
PB1P	PB1P
PR1P	PR1P
Video Out	
SCT Monitor CVBS Out	
VDAC_OUT1	
SCT TV CVBS Out	
NA	
Other	
OPWRSB (O)	OPWRSB (L=PWR On)
VCXO CTRL (O)	NA
Ethernet Port (MIU/RMI)	
RMI	
TXEN	TXEN
TXD0	TXD0
TXD1	TXD1
TXD2	NA
TXD3	NA
TXCLK	TXCLK
COL	COL
RXDV	RXDV
RXD0	RXD0
RXD1	RXD1
RXD2	NA
RXD3	NA
RXCLK	RXCLK
PHY_REF_CLK	
PHY_REF_CLK	
MDC	
MDIO	
PHY RESET#	
SPL_CLK	
External MUX CTRL2	
A0SDATA1	
Audio DAC Out	
AV Audio Out	
Audio DAC1 (PWM1)	
Headphone out	

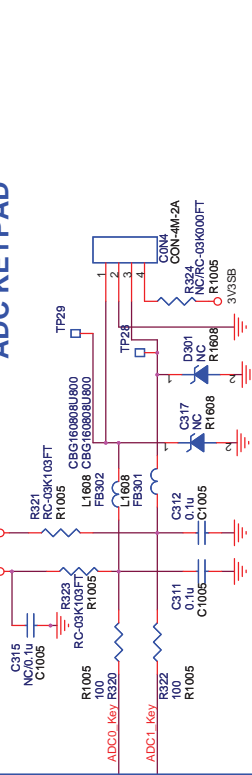
I2S Out	
I2S MCLK(O)	AOMCLK
I2S BCLK(O)	AOBCK
I2S LRCK(O)	AOLRCK
I2S DATA(O)	AODATA0 (Speaker Out)
Audio CTRL	
All_MUTE_CTRL (O)	ADIN14 (H = UN-MUTE)
Head Phone DET (I)	SPL_DATA(L = Plug-In)
SPDIF IN (I)	NA
Speaker Amp CTRL	
Speaker AMP Type	
AD82581B (Digital SP AMP)	
Speaker Amp Mute (O)	
JTRST (L =MUTE)	
AV Input Source	
CVBS0	CVBS0P
CVBS1	SC0
CVBS2	SY1
SY0	CVBS1P
SC0	SY0
SY1	NA
SC1	NA
YPbPr Input Source	
SOY0	SOY0
Y0P	Y0P
PB0P	PB0P
PR0P	PR0P
SOY1	SOY1
Y1P	Y1P
PB1P	PB1P
PR1P	PR1P
Video Out	
SCT Monitor CVBS Out	
VDAC_OUT1	
SCT TV CVBS Out	
NA	
Other	
OPWRSB (O)	OPWRSB (L=PWR On)
VCXO CTRL (O)	NA
Ethernet Port (MIU/RMI)	
RMI	
TXEN	TXEN
TXD0	TXD0
TXD1	TXD1
TXD2	NA
TXD3	NA
TXCLK	TXCLK
COL	COL
RXDV	RXDV
RXD0	RXD0
RXD1	RXD1
RXD2	NA
RXD3	NA
RXCLK	RXCLK
PHY_REF_CLK	
PHY_REF_CLK	
MDC	
MDIO	
PHY RESET#	
SPL_CLK	



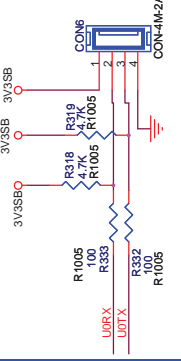
RESET Circuit



ADC KEYPAD



XTAL Dip/SMD Dual Layout

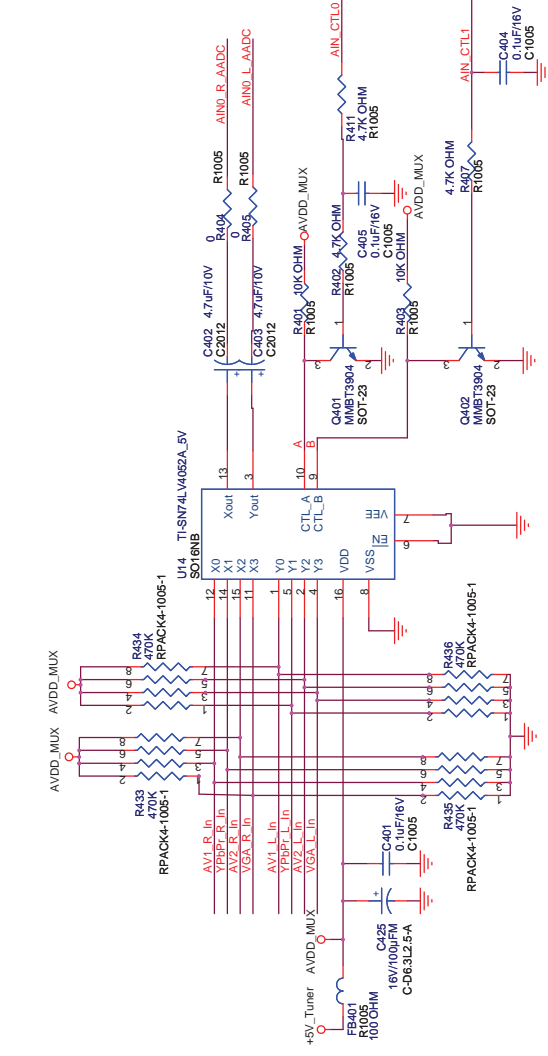


遥控借用LT24830遥控板。



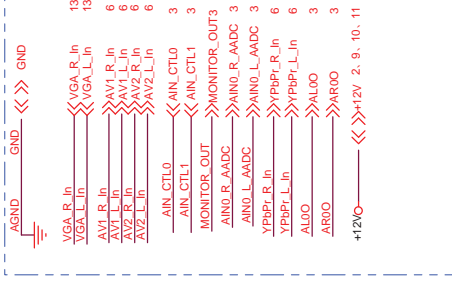
四川长虹多媒体公司液晶所		CHANGHONG	
Title			
IR/KEY/EEPROM/FLASH			
Size	Document Number	Rev	1.0
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Audio MUX



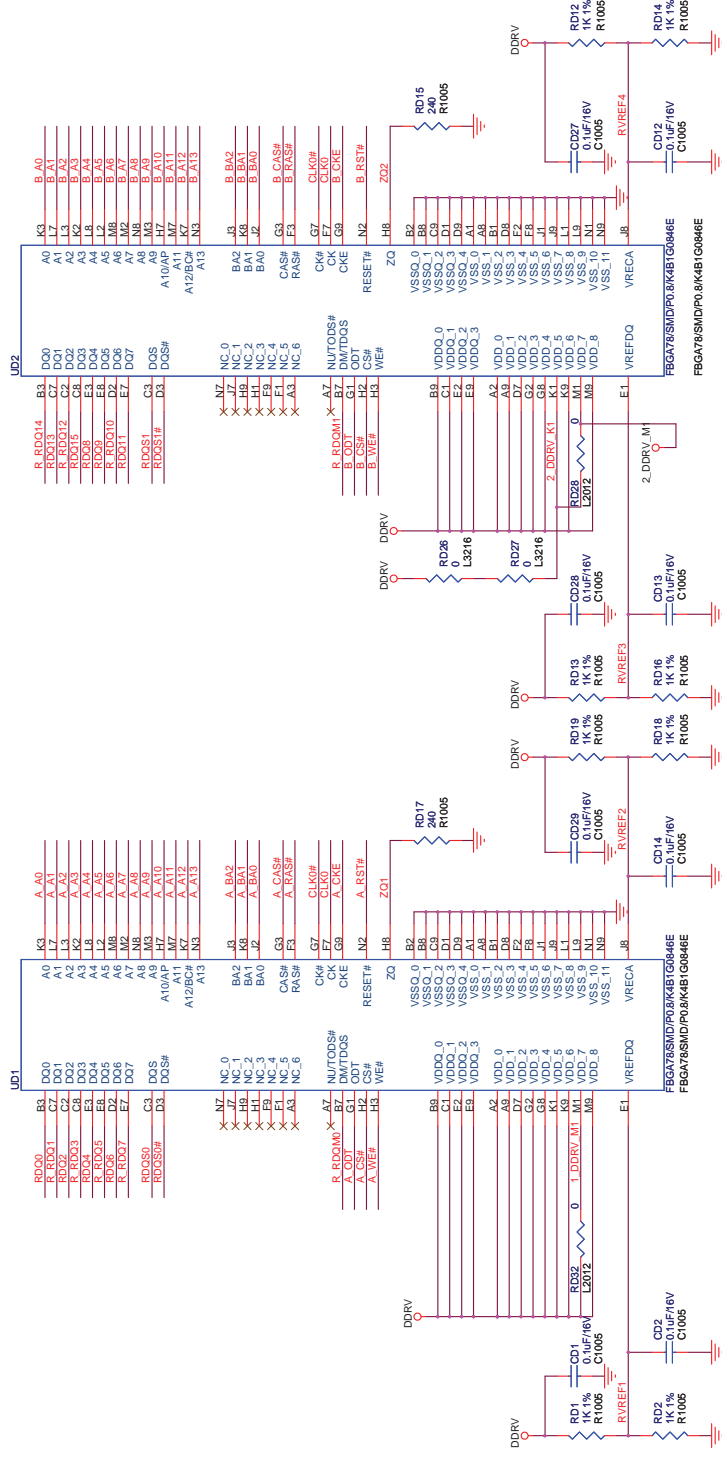
	AIN_CTL1	AIN_CTL0
AV1	1	0
AV2	0	1
YPBR	1	1
VGA	0	0

TP40	CON7	AV-13PKE	AV OUT	R1608	NEARLY IC



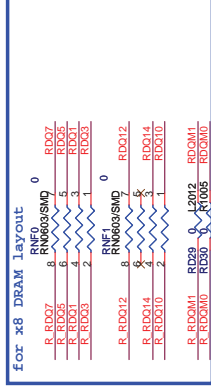
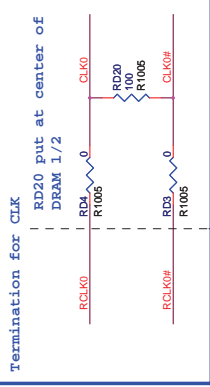
DDR3 X8 #1

DDR3 X8 #2

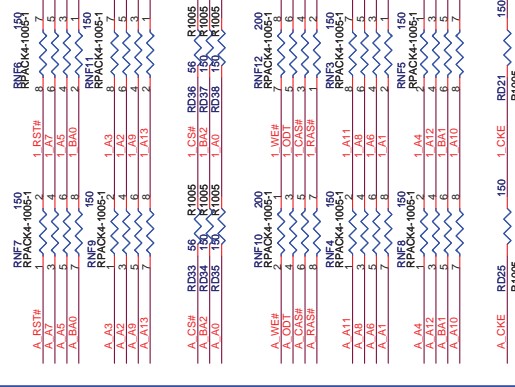


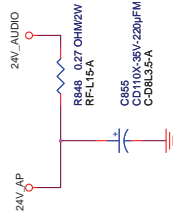
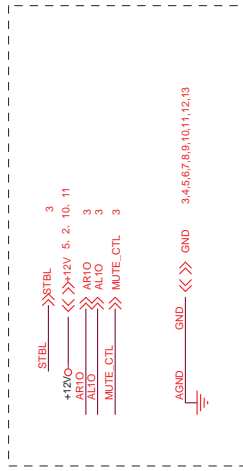
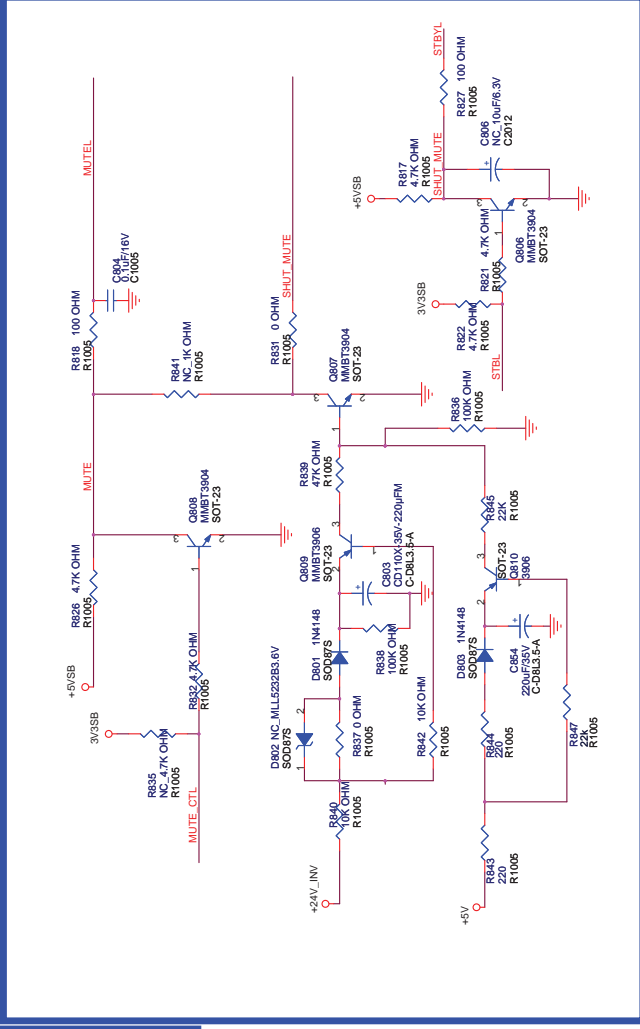
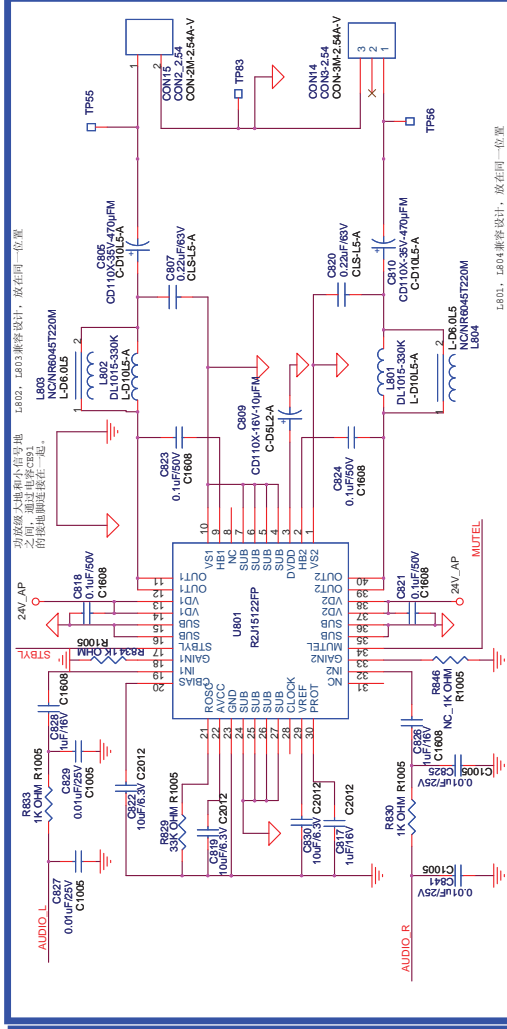
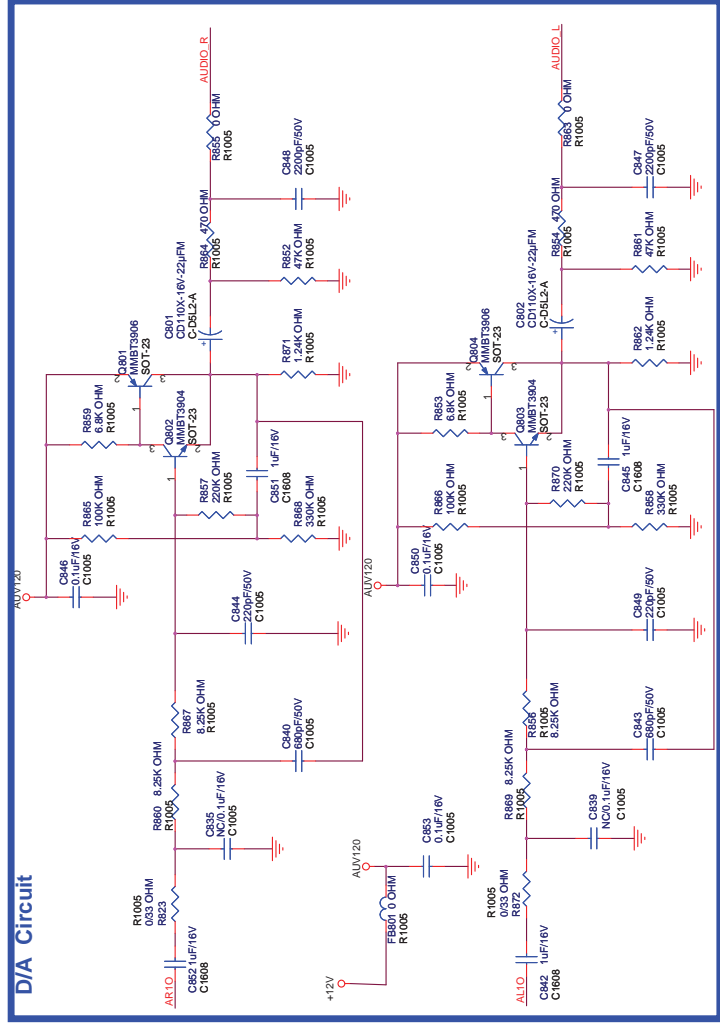
CAPS for DRAM IO Power (Close to DRAM1)

CAPS for DRAM IO Power (Close to DRAM2)

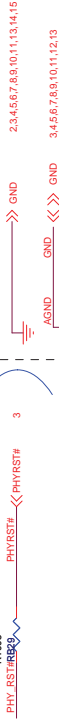


Damping for ADDRESS/CMD

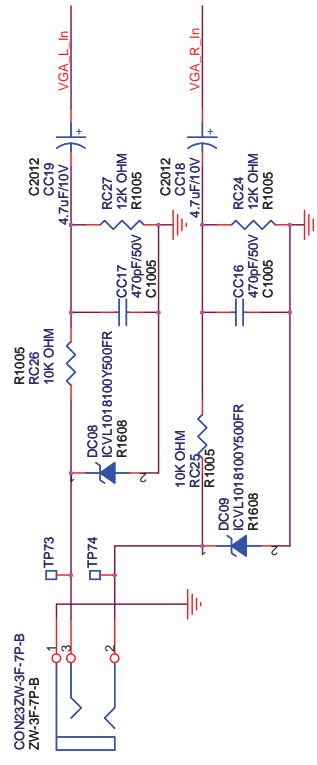
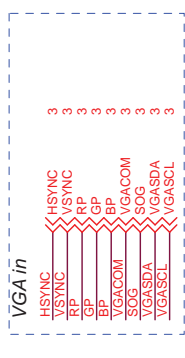




ETHERNET PHY RMII Mode



PHY_RST#RB29 PHY_RST# << PHY_RST# 3



四川长虹多媒体公司液晶所		CHANGHONG	
Title		VGA	
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